

### 10.3 AC jellemzők

SIGNAL	SYMBOL	PARAMETER	MK3886		MK3886-4		UNIT	CONDITIONS
			MIN	MAX	MIN	MAX		
MREQ	$t_W(MRL)$ $t_W(MRH)$	Pulse Width, MREQ Low Pulse Width, MREQ High	480 190		300			
INT	$t_{DX}(IT)$	Delay to falling INT from external interrupt active transition		400		400	n.s.	
	$t_{DT}(IT)$	Delay to INT from Timer Interrupt at rising TCLK		600		600	n.s.	
	$t_{DSRC}(IT)$	Delay to INT from rising edge of SRCLK		900		900	n.s.	
ZCA	$t_{WC}(ZCA)$	Width of ZCA pulse	$P * t_{CLK} - t_{CLK}/2 - 100$				n.s.	
SRCLK	$t_C(SR)$	Period of SRCLK	3.3		3.3		$\mu s$ .	
	$t_S(SI)$	SRIN setup with respect to SRCLK edge	250		250		n.s.	
	$t_H(SI)$	SRIN hold time from SRCLK (x1 mode)	150		150		n.s.	
	$t_{DSR}(SRC)$	Delay to SROUT from SRCLK		2.5		1.9	n.s.	
TCLK	$t_C(TC)$	Period of Timer Clock	400		250		n.s.	
	$t_W(TC)$	Timer Clock Low time	180	220	110	140	n.s.	

### Rendelési információk

PART NO.	DESIGNATOR	PACKAGE TYPE	MAXIMUM CLOCK FREQUENCY	TEMPERATURE RANGE
MK3886N	Z80-COMBO	PLASTIC	2.5 MHz	0° to 70°C
MK3886P	Z80-COMBO	CERAMIC	2.5 MHz	0° to 70°C
MK3886J	Z80-COMBO	CERDIP	2.5 MHz	0° to 70°C
MK3886N-4	Z80-COMBO	PLASTIC	4.0 MHz	0° to 70°C
MK3886P-4	Z80-COMBO	CERAMIC	4.0 MHz	0° to 70°C
MK3886J-4	Z80-COMBO	CERDIP	4.0 MHz	0° to 70°C

